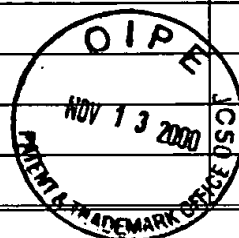


#7

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) (37 CFR 1.98(b))	ATTY. DOCKET NO. 1662-28800 (P00-2998)	SERIAL NO. 09/597,192
	APPLICANTS William P. BUNTON et al.	
	FILING DATE June 20, 2000	GROUP 2034

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U. S. PATENT DOCUMENTS												
Examiner Initial		Patent Number						Issue Date	Patentee	Class	Sub-class	Filing Date (If Appropriate)



FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION													
		Document Number						Publication Date	Country or Patent Office	Class	Sub-Class	Translation	
												Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)			
		AA	Patent Application Serial No. 09/596,980, filed June 20, 2000, and entitled "High-Speed Interconnection Adapter Having Automated Lane De-Skew" by William P. Bunton, John Krause, Patricia L. Whiteside and Scott Smith.
		AB	Patent Application Serial No. 09/597,190, filed June 20, 2000, and entitled "High-Speed Interconnection Link Having Automated Lane Reordering" by William P. Bunton, John Krause and Patricia L. Whiteside.
21		AC	"A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code," IBM J. Res. Develop., Vol. 27 No. 5, September 1983 by A. X. Widmer and P.A. Franaszek, pp. 440-451, Tables (6 p.).
21		AD	"Compaq's InfiniBand™ Architectural Strategy," Technology Brief, March 2000, pp. 1-11.
Examiner <u>Young Sun</u> Date Considered <u>2/18/04</u> EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			